

Speed up your circuit design process with EPHD behavioral models



Solution Overview

Accurate behavioral models are indispensable tools to speed up the circuit design process of advanced power amplifiers, such as Doherty and multi-stage amplifiers. AMCAD's Enhanced PHD (EPHD) behavioral model is the industry's most robust and accurate model for transistors and components, including both impedance interpolation and extrapolation as well as frequency interpolation, and leads to better circuit designs. Since EPHD models are extracted directly from time-domain (large-signal, nonlinear) load pull measurements, models can be extracted up to 10x faster than the industry-standard solution. EPHD is also compatible with most commercial VNAs, allowing the average engineer access to state-of-the-art behavioral modeling capabilities and result in best-in-class circuit designs.



Features

- > Supports modeling for all classes of amplifier operation
- > Supports impedance interpolation and extrapolation; frequency interpolation
- > Valid for small-signal and large-signal/nonlinear simulations
- > Measurements compatible with most commercial VNAs and load pull systems
- > Model compatible with ADS and Microwave Office



Benefits

- > Design better circuits with more robust behavioral models
- > Extract behavioral models 5x-10x faster than the industry-leading solution
- > Lower implementation costs by extracting models directly from time-domain load pull measurement data
- > Increase access to behavioral model extraction by empowering time-domain large-signal analysis on most commercial VNAs



Applications

- > Advanced circuit design
- > Building behavioral model libraries
- > System-level simulation